

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 57454-972		SERIAL NO. Divisional of Appln. Serial No. 10/135,459	
				APPLICANT Hideto HIDAKA			
				FILING DATE September 17, 2003		GROUP Not yet assigned	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
<i>ap</i> <i>ap</i> <i>ap</i>		Scheuerlein, et al., "A 10ns Read and Write Non-Volatile Memory Array Using A Magnetic Tunnel Junction and FET Switch in Each Cell" ISSCC Digest of Technical Paper, TA 7.2, February 8, 2000, pp. 94-95, 128-129 and 409-410.					
		Durlam, et al., "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements" ISSCC Digest of Technical Papers, TA7.3, February 8, 2000, pp. 96-97, 130-131 and 410-411.					
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EXAMINER <i>Chih Phung</i>				DATE CONSIDERED 4/5/04			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.